

Curriculum Vitae

Dr. KHURRAM JAVED



PERSONAL INFORMATION

Current Address: Electrical Engineering Department, Fast, Islamabad
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EDUCATIONAL QUALIFICATION

2012-2017: **PhD. Electronics Engg. from Hanyang University, S. Korea.**
2009-2011: **MS. Electronics Engineering from GIK institute, Swabi. Pakistan**
2003-2007: **Bsc. Electronics Engineering with from CIIT Abbottabad. Pakistan**

JOURNAL PUBLICATIONS

- J. S. Kim, K. Javed and J. Roh, "Design of a Low-Power and Area-Efficient LDO Regulator Using a Negative-R-Assisted Technique," in IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 70, no. 10, pp. 3892-3896, Oct. 2023.
- J. S. Kim, K. Javed, K. H. Min and J. Roh, "A 13.5-nA Quiescent Current LDO With Adaptive Ultra-Low-Power Mode for Low-Power IoT Applications," in IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 70, no. 9, pp. 3278-3282, Sept. 2023.
- J. Moon, J. Lee, K. Javed, J. Hong, and J. Roh, "Concurrent Current and Voltage Regulated Buck-Boost Converter for Automotive LED Matrix Headlights", IEEE Trans. Power Electronics, vol. 38, no. 5, pp. 6015-6023, May. 2023.
- Saad Rasheed, Muhammad Usman, Shazma Ali, Hamid Ali, Laraib Mustafa and Khurram Javed, "Carrier Confinement in Ultraviolet-C AlGaIn Light-Emitting Diodes", ecs journal of solid state science and technology, Vol. 12, no. 4, 2023.
- K. Javed, J. Yeo, J. Lee, and J. Roh: "A fast-transient dc-dc converter using high-performance error amplifier with a rapid output-voltage control technique", International Journal of Circuit Theory and Applications, vol. 45, issue 10, Pages 1431-1438, 2017.
- K. Javed, and J. Roh: "LDO regulator with high power supply rejection at 10 MHz," ELEX IEICE, vol. 13, no. 24, pp. 1-6, 2016.
- J. Yeo, K. Javed, J. Lee, J. Roh, and J. Park, "Capacitorless low-dropout regulator with enhanced slew rate and 4.5-μA quiescent current," Analog Integrated Circuits and Signal Processing, vol. 90, no. 1, pp. 227-235, 2017.

PATENTS

- I kim, J Roh, K Javed, "LDO regulator with high bandwidth PSR," 10-2017-0043976.

WORK EXPERIENCE

- Assistant Professor at FAST, Islamabad, Pakistan since September 2025.
- Assistant Professor at NUST, Islamabad, Pakistan from January 2024 to September 2025.
- Assistant Professor at IST, Islamabad, Pakistan from December 2017 to December 2023.
- Assistant Professor at PIEAS, Islamabad, Pakistan from April 2017 to December 2017.
- Lecturer at University of Faisalabad, Pakistan from August 2011 to August 2012.

RESEARCH INTERESTS

- Analog and mixed signal IC design
- Power management IC design
- Microelectronics

Courses Taught

- Advanced Electronics
- Advanced Power Electronics
- Mixed Signal IC design
- Advanced Embedded Systems
- Embedded Systems
- Power Electronics
- Electronics Circuits and Devices
- Circuit Analysis

Trainings Conducted

- Training on Data converter IC design at NUST Chip design Center, Pakistan
- Seminars on power management IC design at Hanyang university, S. Korea and IST, Pakistan

Fundings and Theses Supervision

- Successfully acquired funding for few projects related to power management IC design and converters
- Currently co-supervising two PhD theses on CMOS LDO design